

# MULTILAYER CHIP SUPPRESSORS (CHIP BEADS) 積層式晶片磁珠

## INTRODUCTION 產品介紹

Multilayer chip beads are SMD components that cover a wide impedance characteristic. Their impedance characteristics mainly comprise resistive components(R), so when an element is inserted in series with a noise conduction passage, the noise content can be attenuated.

積層式晶片磁珠係表面黏著式晶片，涵蓋大範圍阻值，其阻值特性主要包含抵抗性元件，所以當元件被安裝於一有雜訊傳導之通路，其雜訊可被衰減。

## FEATURES 特色

1. These small size chips generating high impedance.  
尺寸小同時產生高阻值
2. Either flow or reflow soldering methods can be use due to electroplating of the terminal electrodes.  
由於外電極係電鍍而成，適用 flow 及 reflow 兩種銲錫性方式。
3. High reliability due to an entirely monolithic structure.  
單石結構具有高信賴性。
4. Low DC resistance structure of electrode prevents wasteful electric power consumption.  
電極之低直流阻抗可避免無謂之電力消耗。

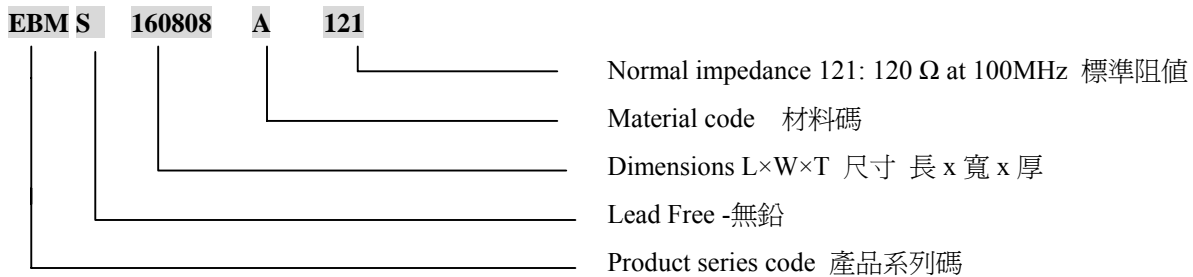
## APPLICATION 產品應用

To suppress EMI/RFI and to prevent self-oscillation in electronic products such as:

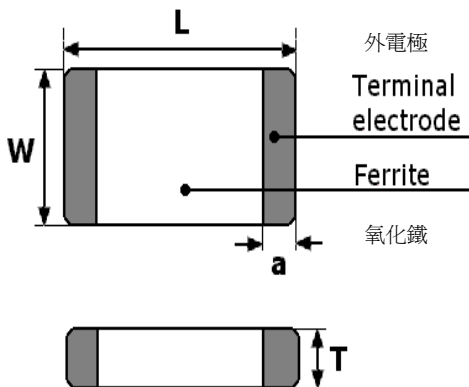
為抑制 EMI/RFI 及避免電子產品之自我振盪，例如：

1. Computers and peripheral equipment. 電腦及其週邊設備
2. VCRs, Television, Pagers. 錄放影機，電視機，呼叫器
3. Cellular phones. 手提電話
4. Digital communication equipment. 數位通訊設備
5. Various electronics equipments. 各式電子設備
6. Circuit where a stable ground is unavailable. 缺乏穩定基板之電路

## PART NUMBERS SYSTEM 編碼系統



## SHAPES AND DIMENSIONS 形狀及尺寸



SHAPE 尺寸	L長 m/m	W寬 m/m	T厚 m/m	a 銀寬 m/m
100505(0402)	1.0±0.05	0.5±0.05	0.5±0.05	0.1~0.3
160808(0603)	1.6±0.15	0.8±0.15	0.8±0.15	0.2~0.6
201209(0805)	2.0±0.2	1.25±0.2	0.9±0.15/-0.2	0.2~0.8
321611(1206)	3.2±0.2	1.6±0.2	1.1±0.2	0.4~1.0
321616(1206H)	3.2±0.2	1.6±0.2	1.6±0.2	0.4~1.0
322513(1210)	3.2±0.2	2.5±0.2	1.3±0.2	0.6~1.0
451616(1806)	4.5±0.2	1.6±0.2	1.6±0.2	0.6~1.0
453215(1812)	4.5±0.2	3.2±0.2	1.5±0.2	0.6~1.0

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